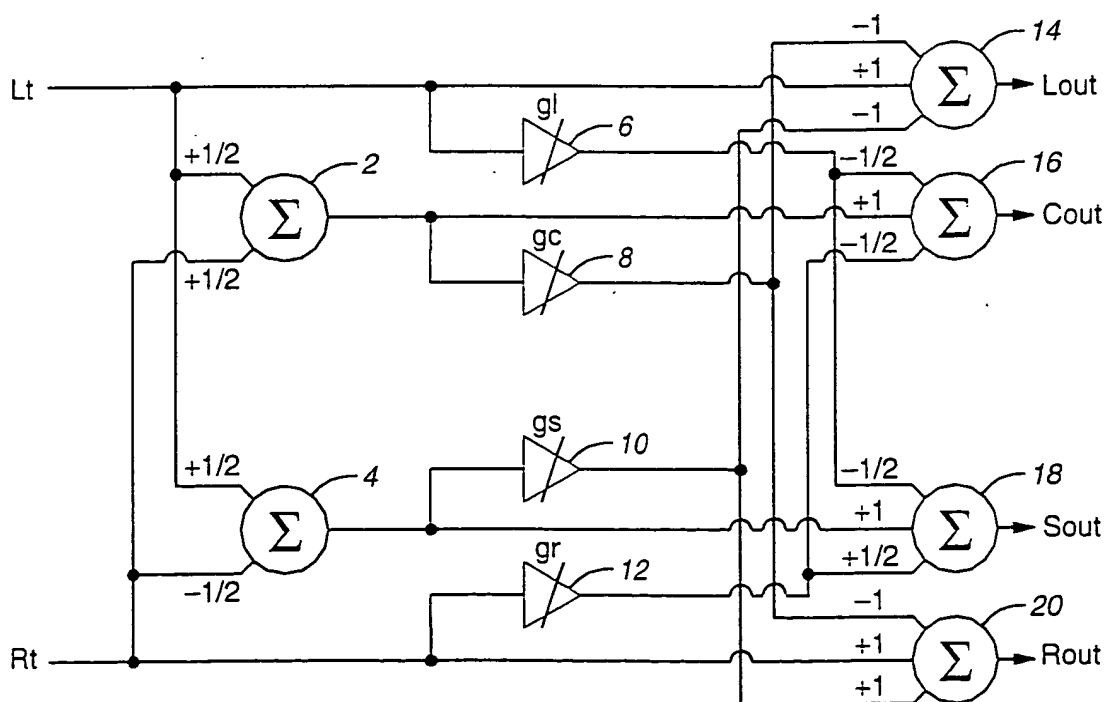


**FIG. 1**  
**PRIOR ART**



**FIG. 2**  
**PRIOR ART**



+

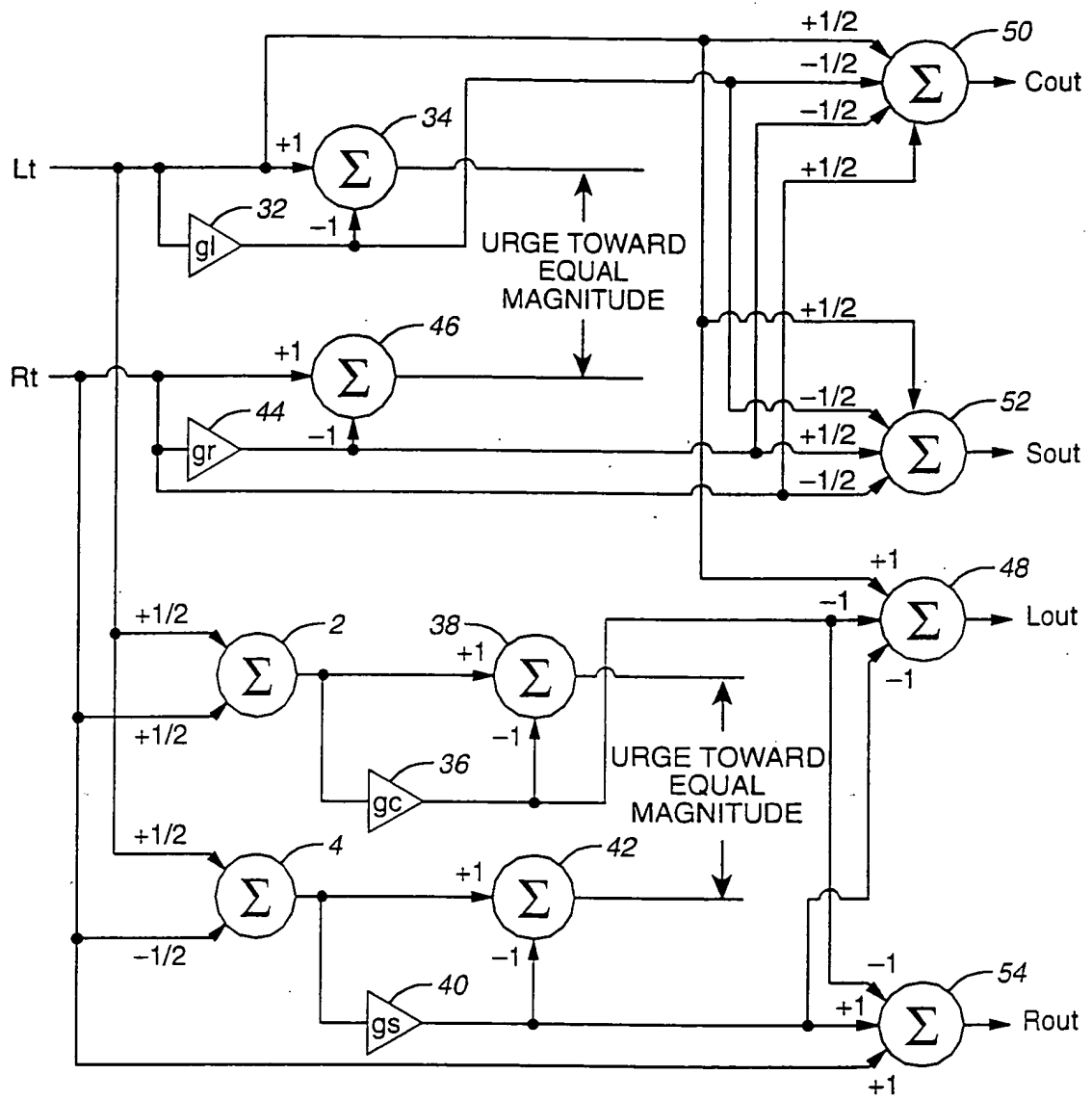
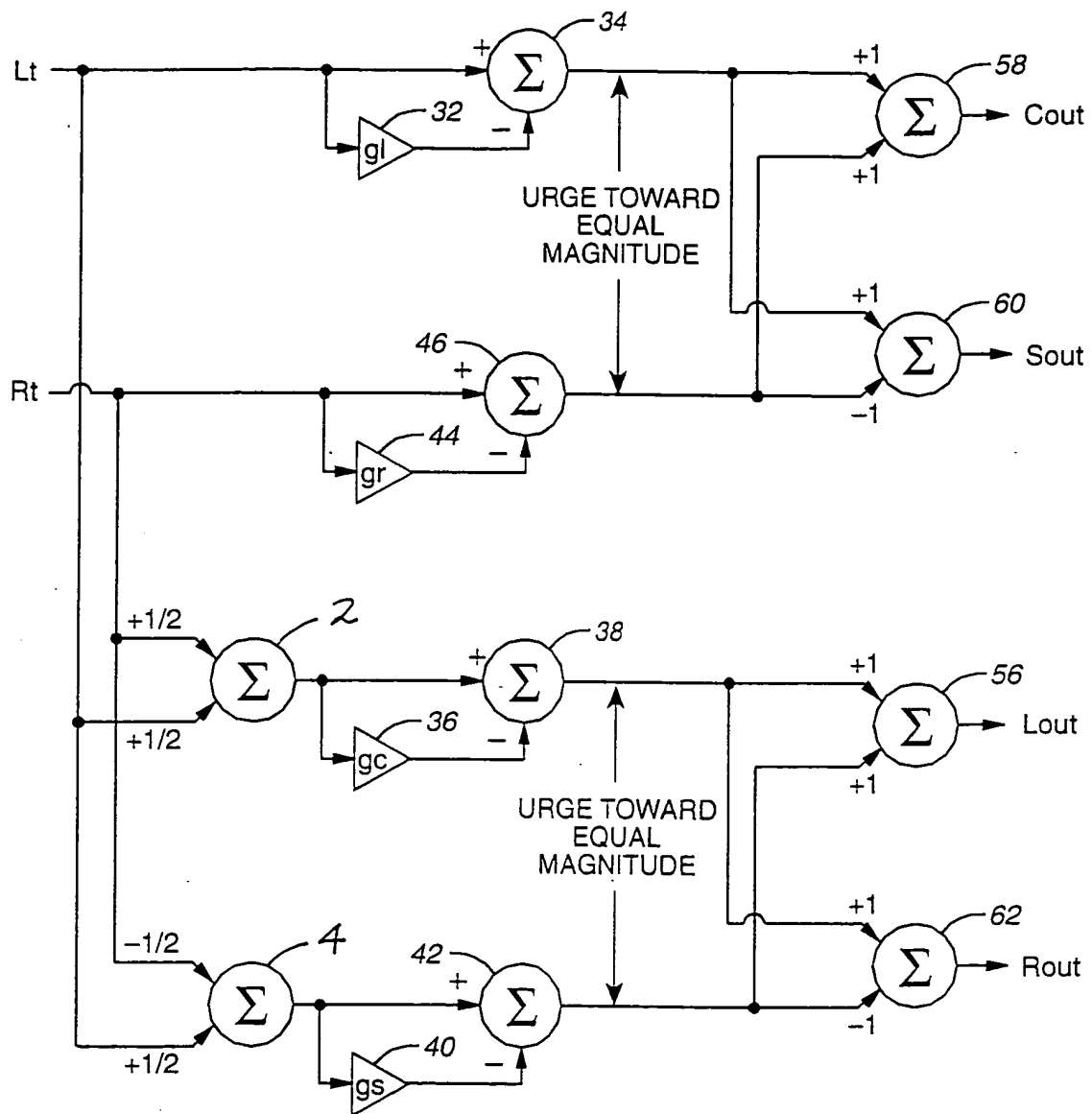
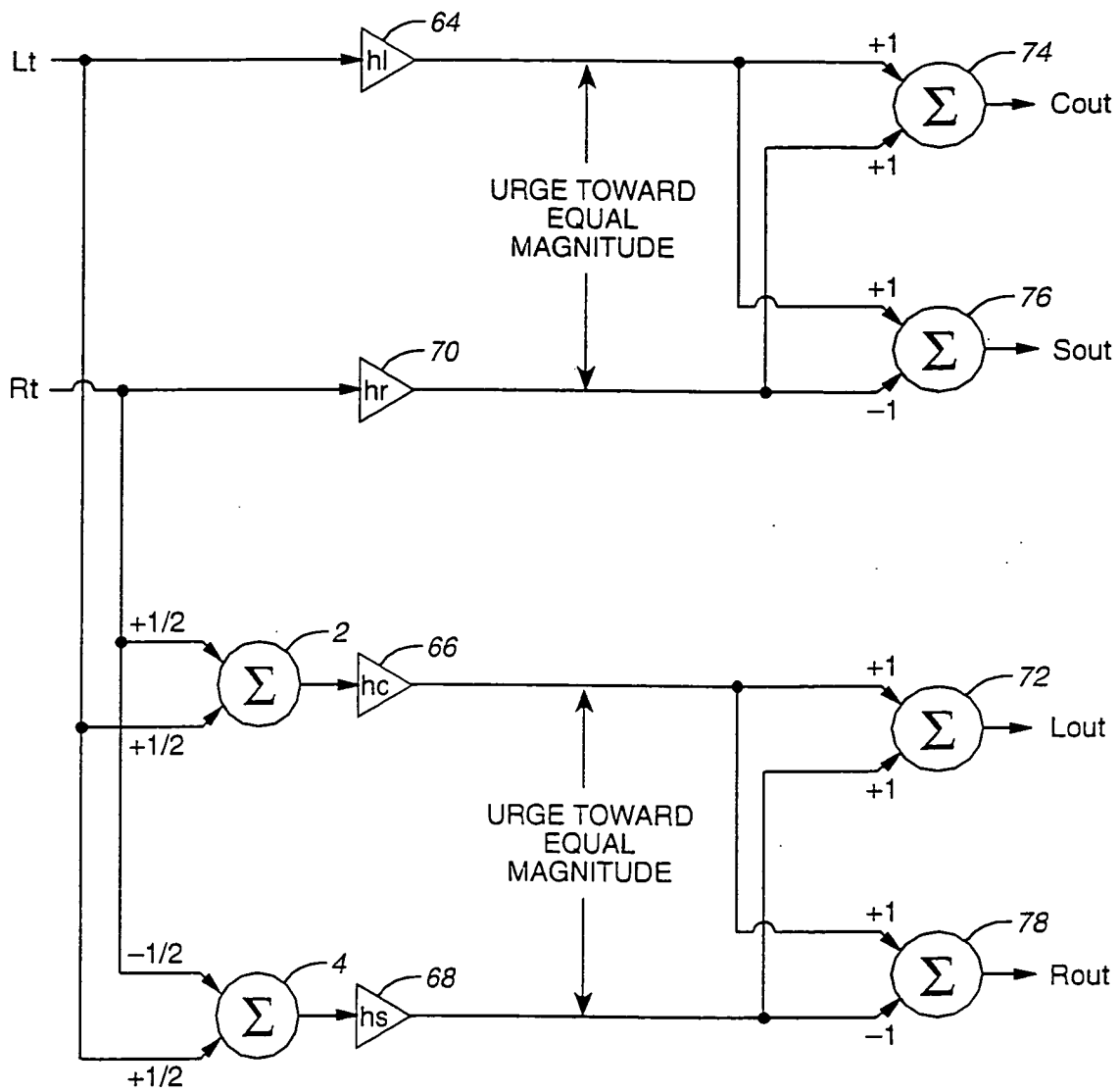


FIG.\_4

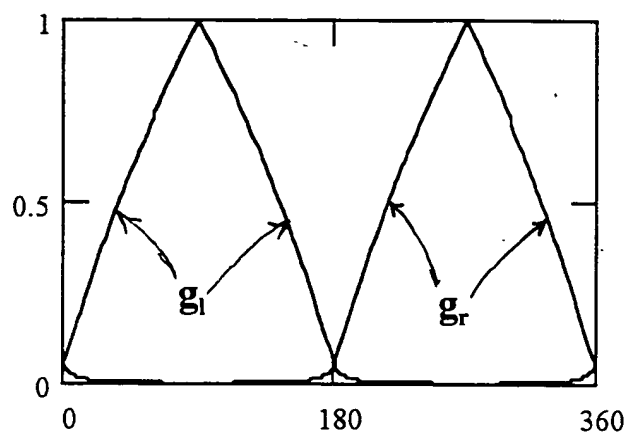
+



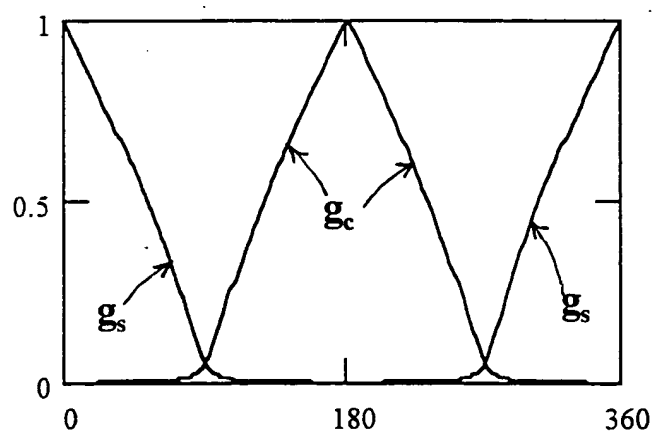
**FIG.\_5**



**FIG.\_6**



**FIG. 7**



**FIG. 8**

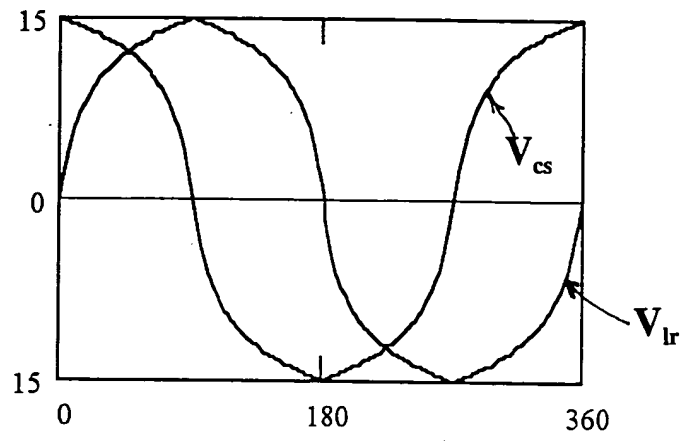


FIG. 9

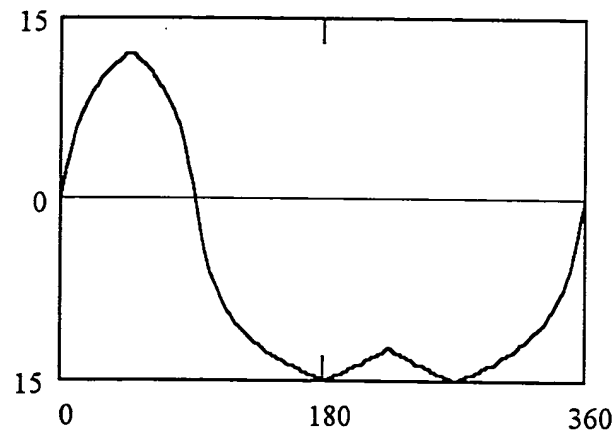


FIG. 10

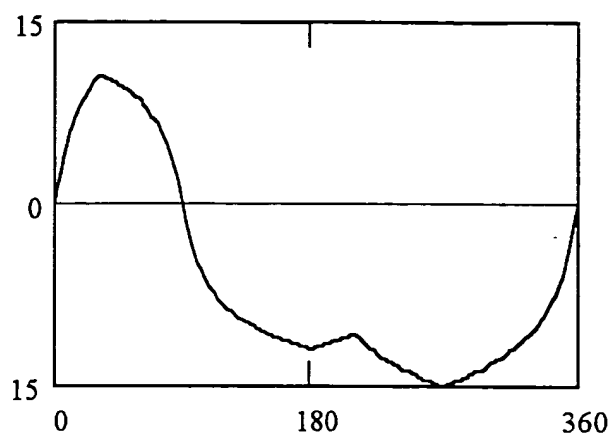


FIG. 11

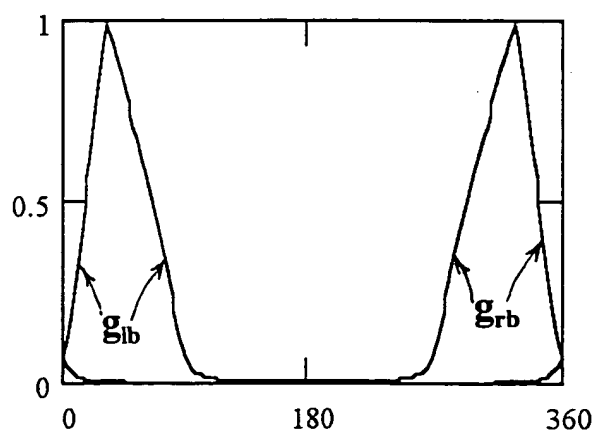


FIG. 12



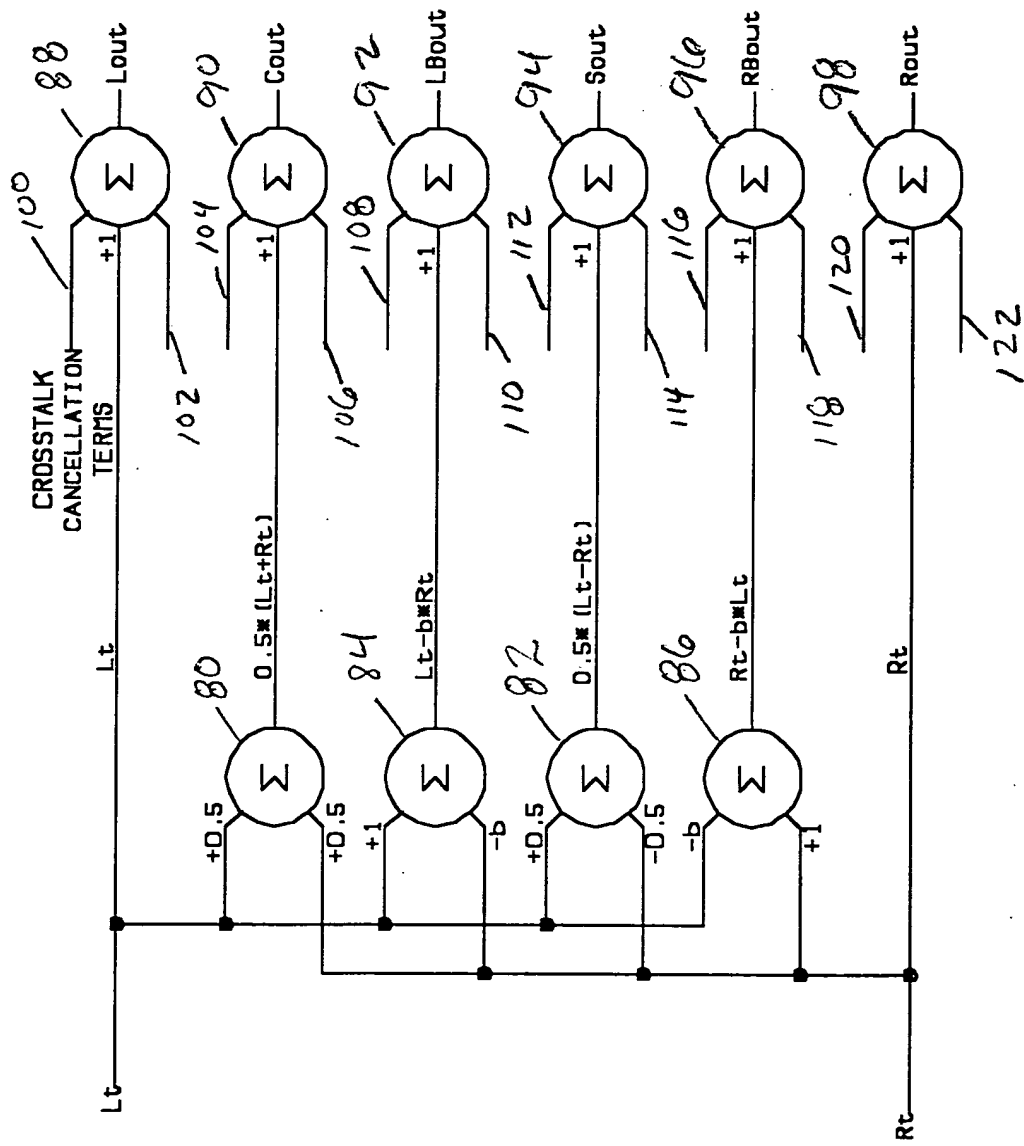


FIG. 13

146  
182  
178

<i>FIG. 15A</i>	<i>FIG. 15B</i>	<i>FIG. 15C</i>	<i>FIG. 15D</i>	<i>FIG. 15E</i>
<i>FIG. 15F</i>	<i>FIG. 15G</i>	<i>FIG. 15H</i>	<i>FIG. 15I</i>	<i>FIG. 15J</i>
<i>FIG. 15K</i>	<i>FIG. 15L</i>	<i>FIG. 15M</i>	<i>FIG. 15N</i>	<i>FIG. 15O</i>

**FIG. 15**

**Inputs**

Left\_1  
Right\_1  
Left\_2  
Right\_2

J1 RCA  
J2 RCA  
J3 RCA  
J4 RCA

SW1-1  
SW1-2  
4POT

R3 49.9K  
R4 49.9K  
R19 49.9K  
R20 49.9K

C3 82pF  
C6 82pF

U1A TL074  
U1D TL074

R1 365K  
R2 2M  
R13 365K  
R16 2M

100K Input "Z"

Right VCA Gain  
VR3 5K  
R38 15K

D1 1N4736B 6.8V  
D2 1N4736B 6.8V  
R41 48.4K  
C15 .022  
U5B TL074  
LIN Correction

R42 200K  
R43 10K  
R49 10K  
R53 200K

Left VCA Gain  
VR5 5K  
R88 15K

**Logic Display**

Front  
Left  
Right  
Back

D3 LED  
D4 LED  
D5 LED  
D6 LED

R86 48K 5%  
R92 18K 5%

**Log Amp Floor Adj.**

Low Level Logic Floor  
27KHz Sq. Wave Gen.

U100 TL074  
R80 82.5K  
R83 10K  
R87 100K  
R82 1M  
VR6 5K

FIG. 15A

FIG. 15A

FIG. 15B

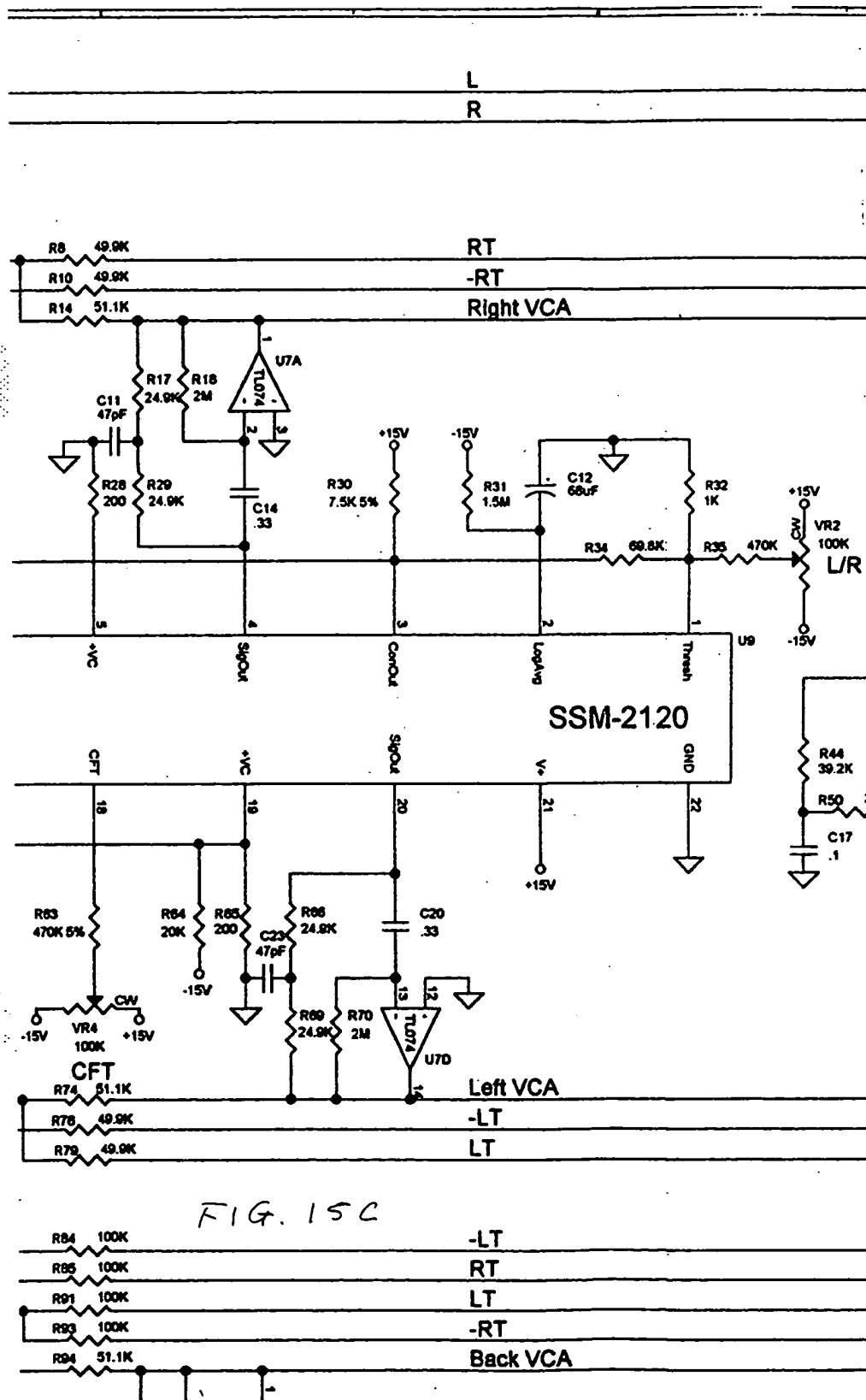
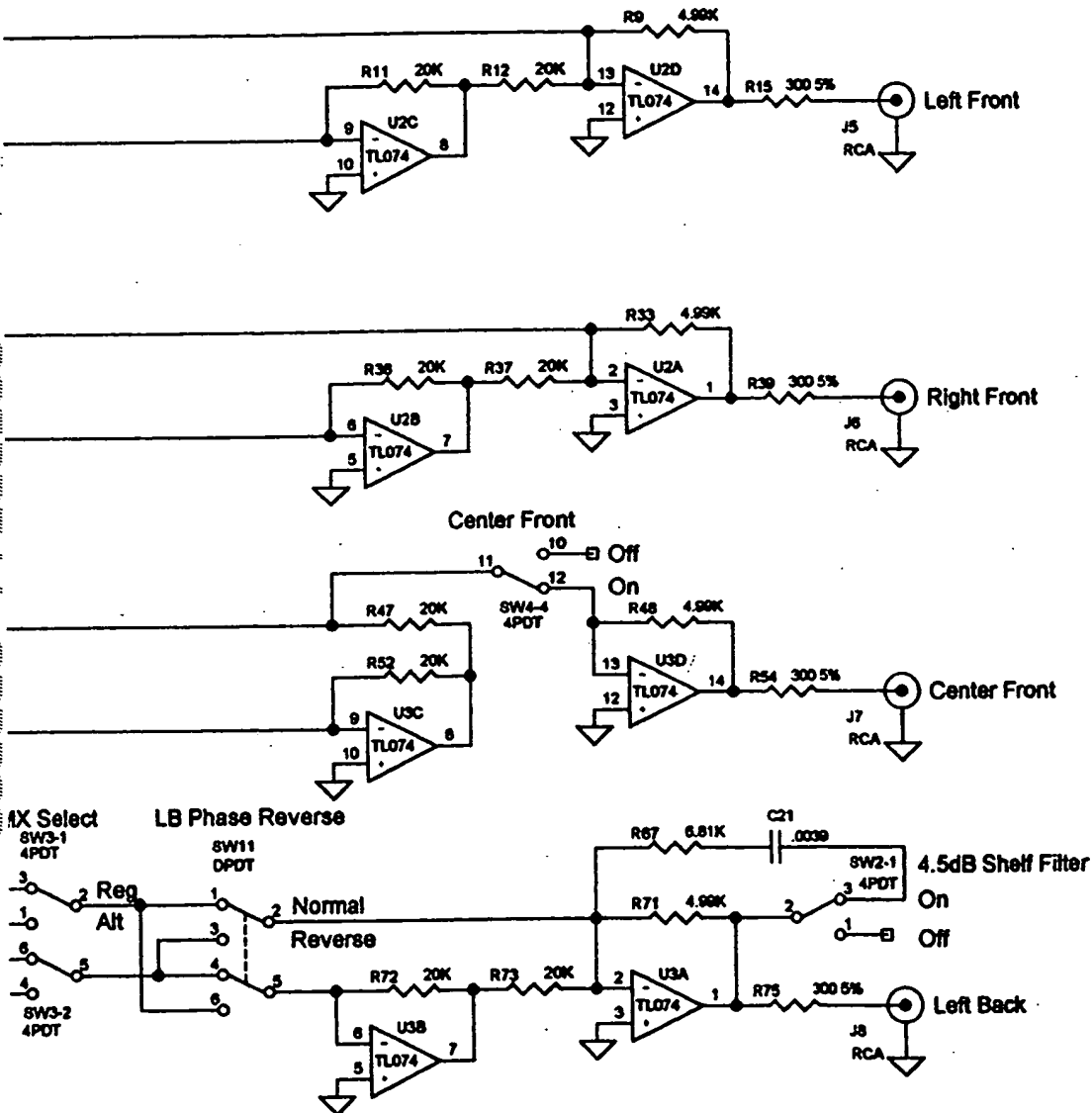


FIG. 15C

FIG. 15D

# Outputs

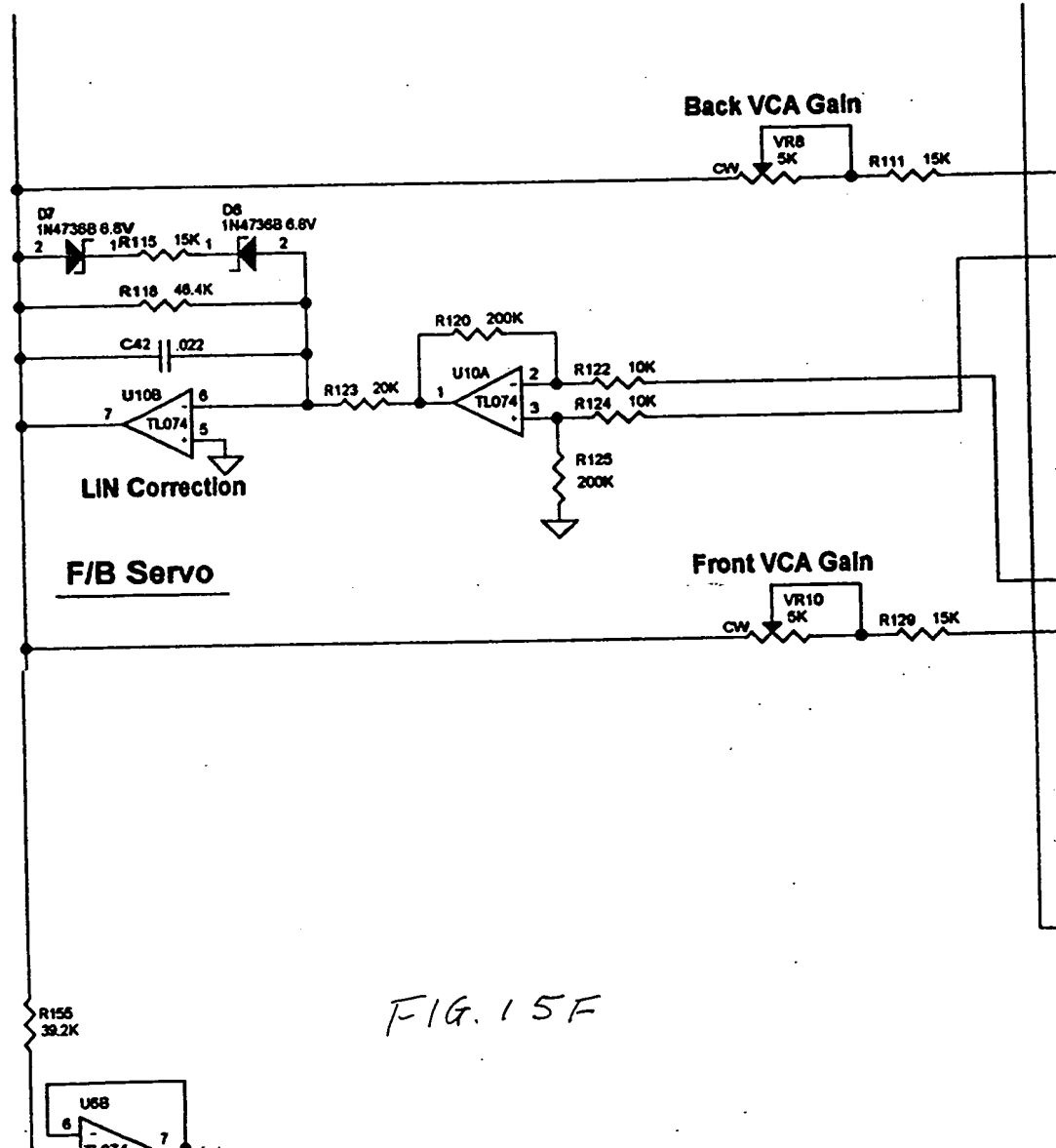


MX Select Alt is for  
3 Back Channel  
Version

FIG. 15E



00000-1425500



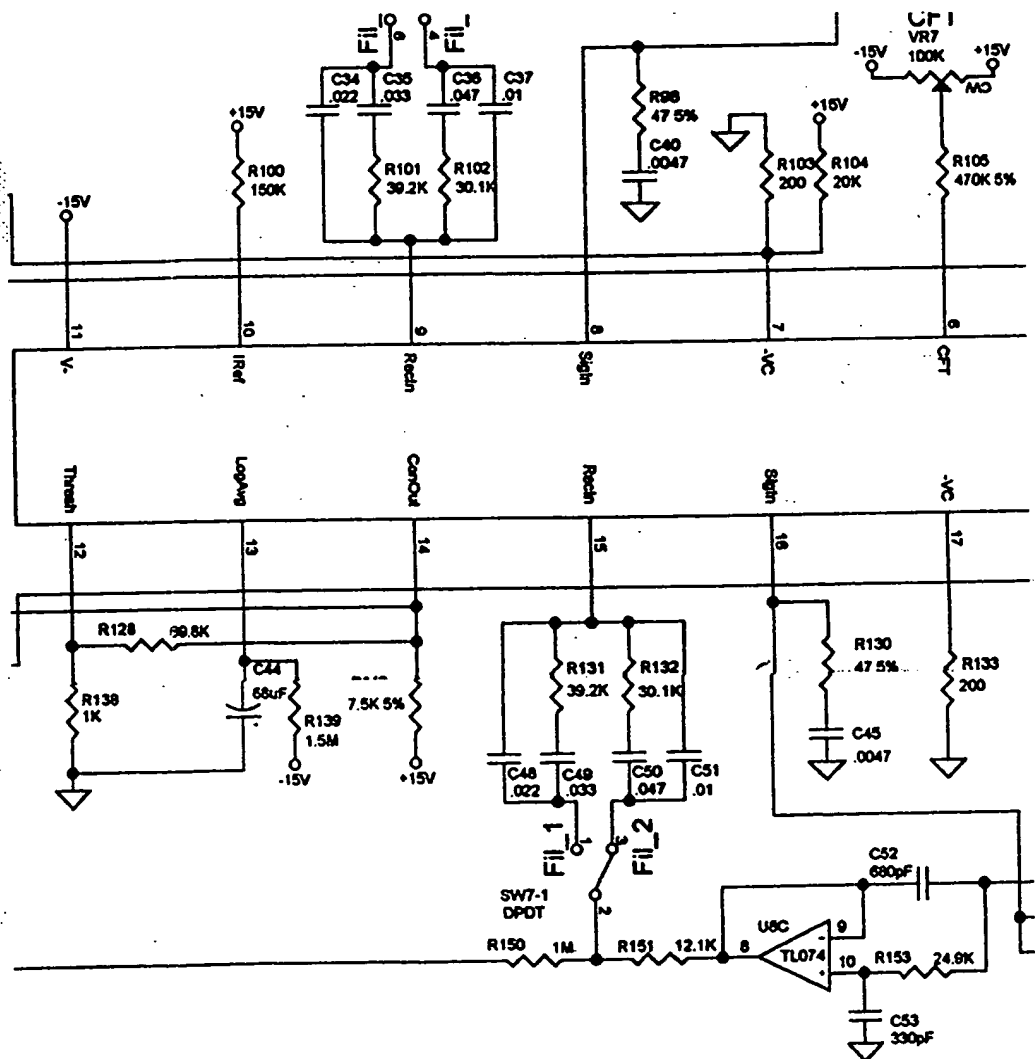
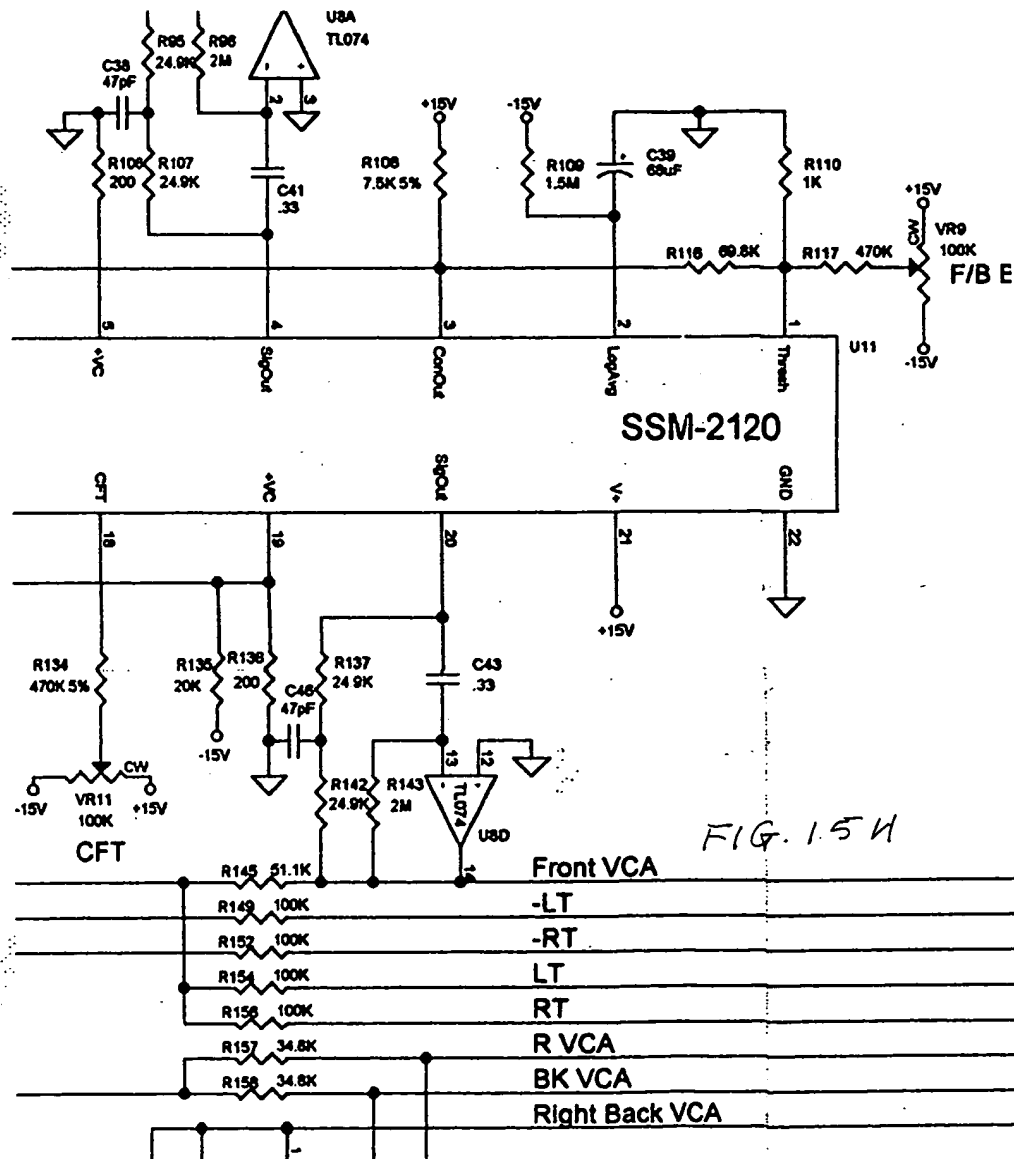


FIG. 15G

003324-03200



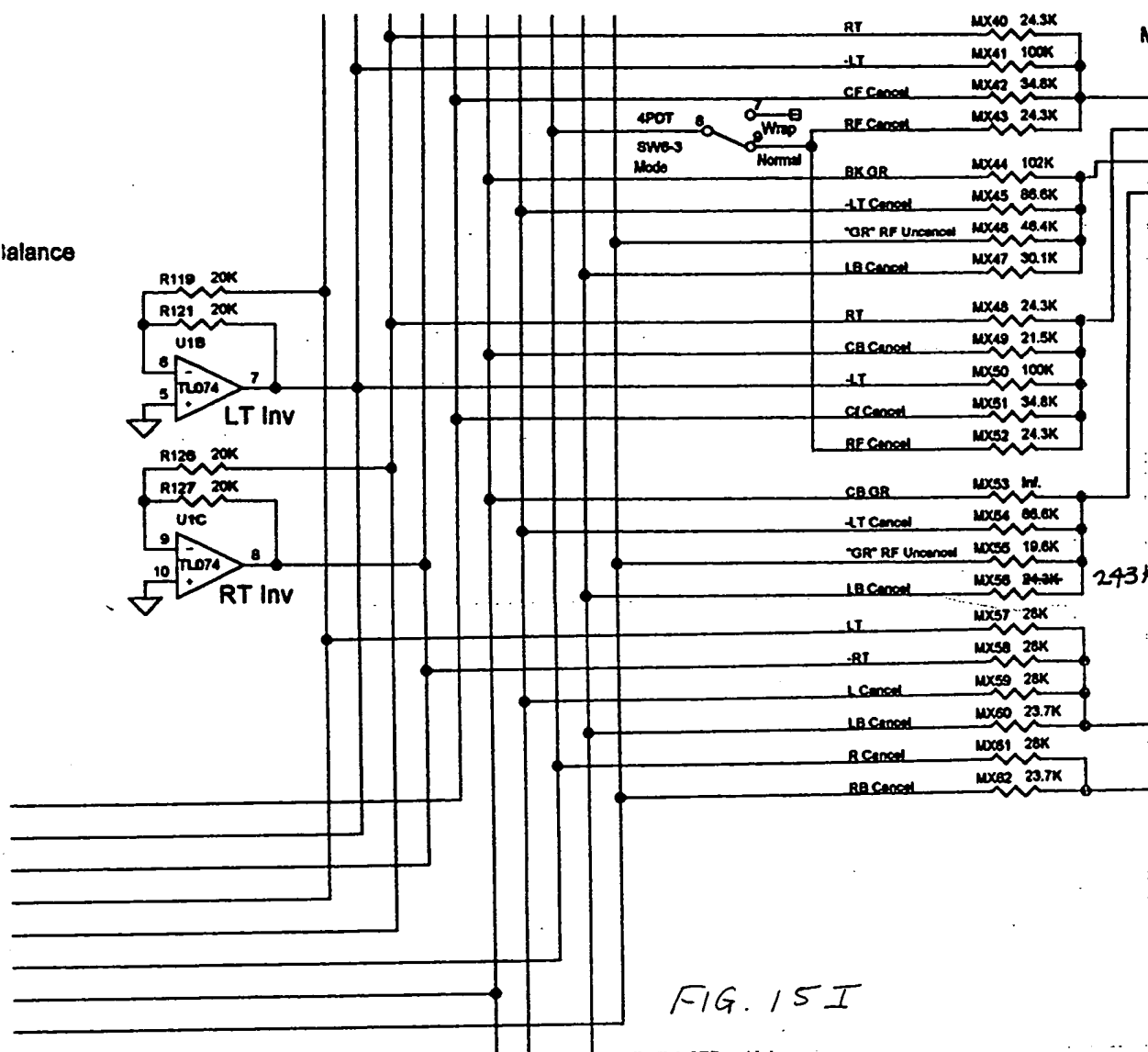
**balance**

FIG. 15I

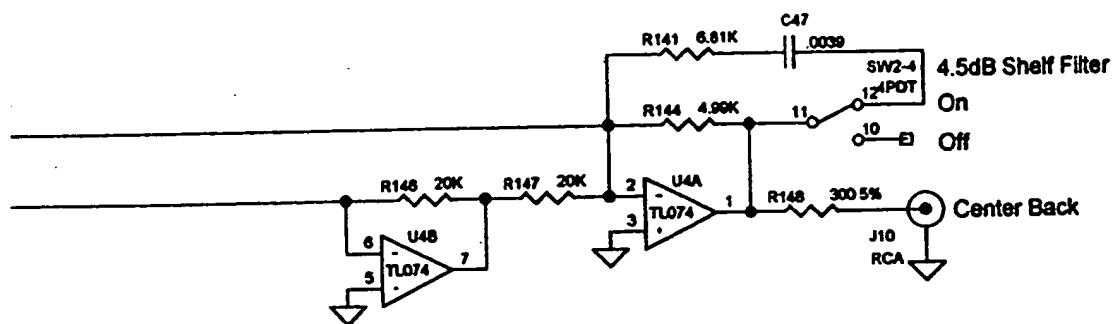
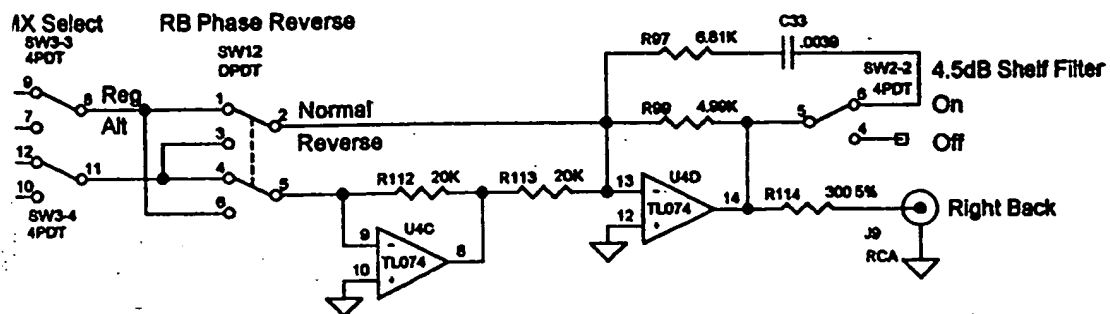
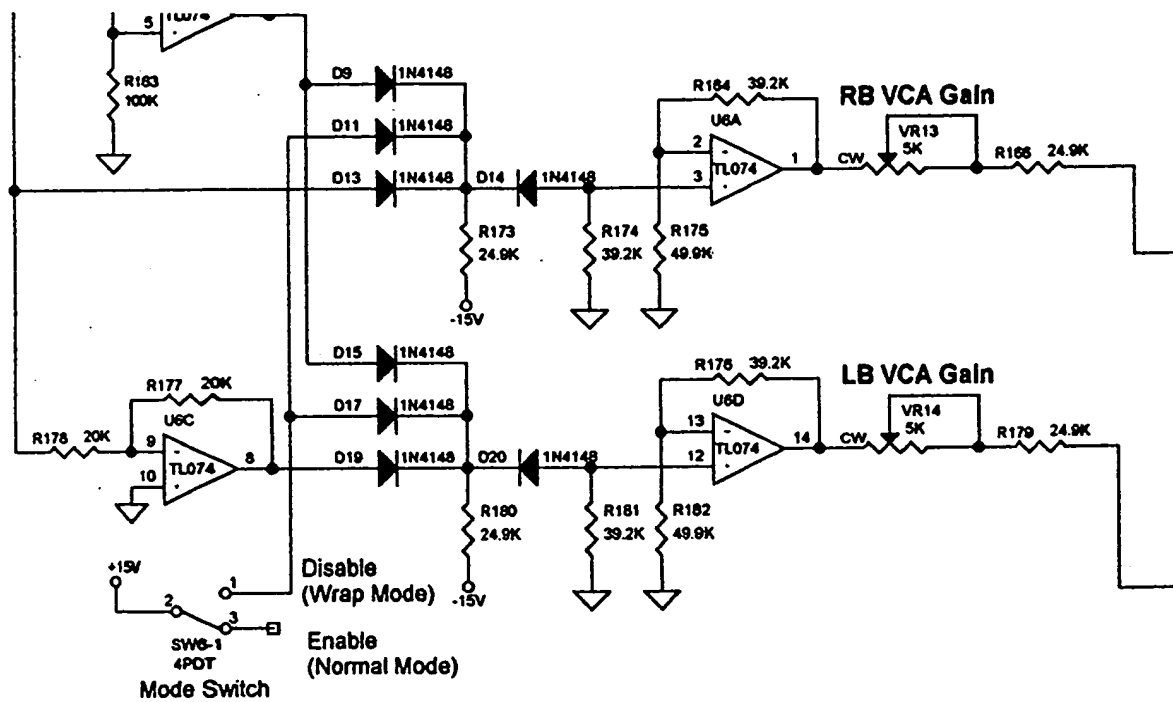


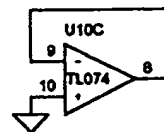
FIG. 15J

00000 11222500



Back Corner Logic

FIG. 15K



09532744-03200

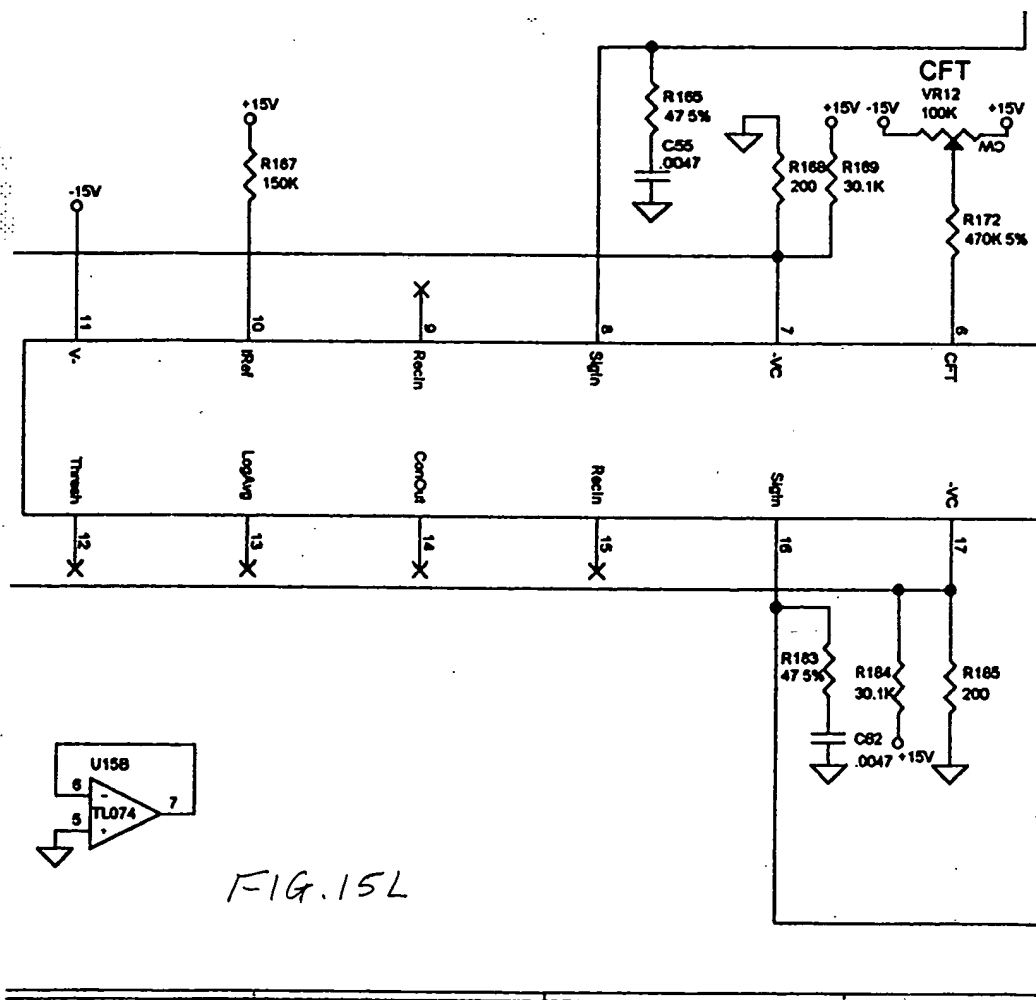
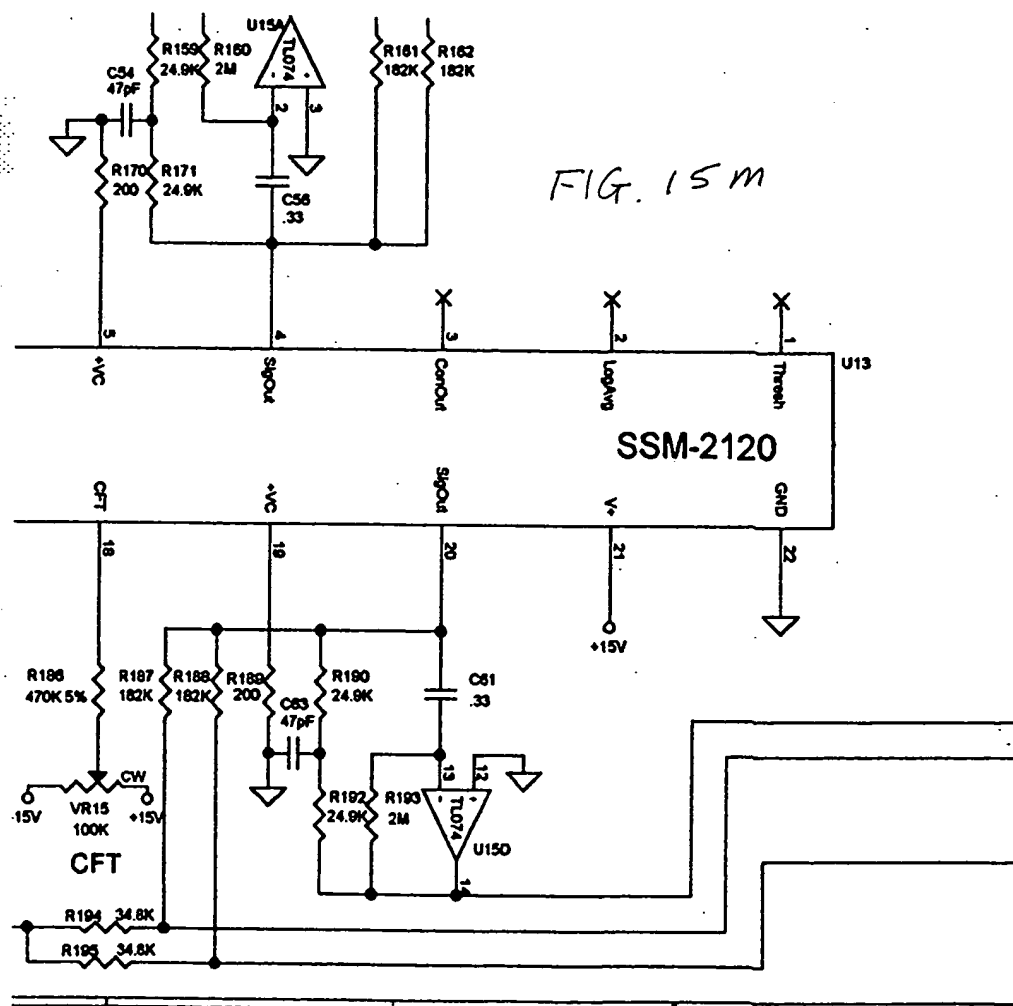


FIG.15L

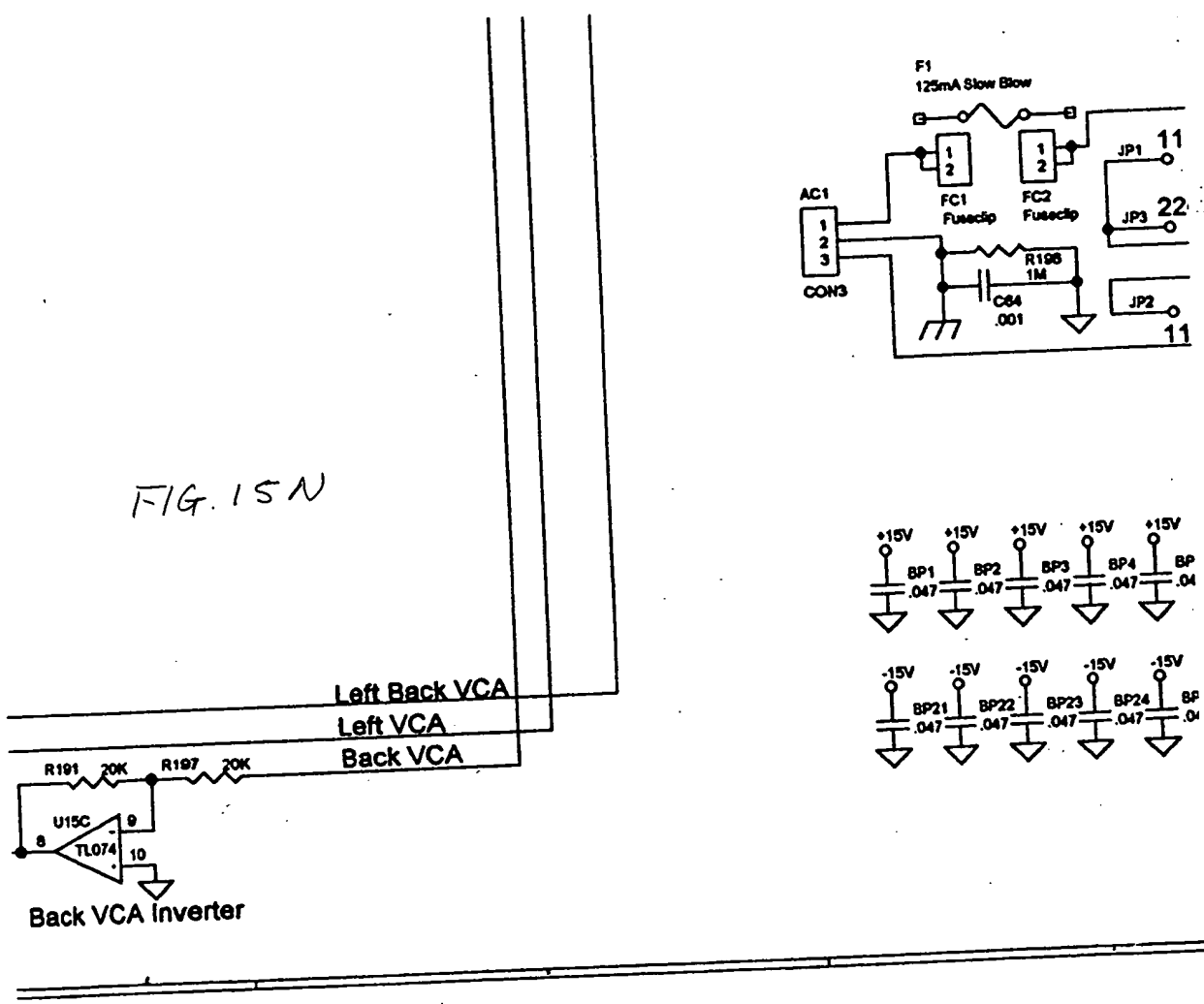
0063244-032000



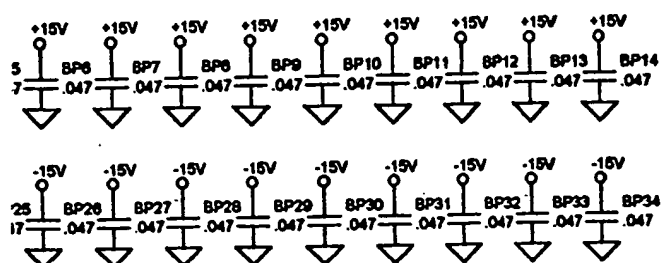


000000 11200000

FIG. 15N



The diagram shows a dual-polarity power supply circuit. It begins with a transformer having a center tap connected to ground (0V). The primary winding is connected to a 0V line. The secondary winding has terminals 1, 2, 3, and 4. Terminals 1 and 2 are connected to ground (0V). Terminals 3 and 4 are connected to the secondary winding. The secondary winding is connected to a bridge rectifier consisting of four diodes (D10, D12, D16, D18, all 1N4003). The positive output of the rectifier is connected to the VIN pin of the first LM7815 regulator (U12). The negative output of the rectifier is connected to the VIN pin of the second LM7815 regulator (U14). The GND pins of both regulators are connected to the center tap of the transformer. The VOUT pins of both regulators are connected to the output terminals, which are labeled +15V and -15V. The output capacitors (C57, C58, C59, C60, all 1000uF 25V) are connected between the output terminals and ground.



Title			
6-Channel Dolby Surround			
Size	Document Number		Rev
Custom (Doc)			PO
Date:	Thursday, October 07, 1999	Sheet	1 of 1